

Fig. 1 (PRIOR ART)

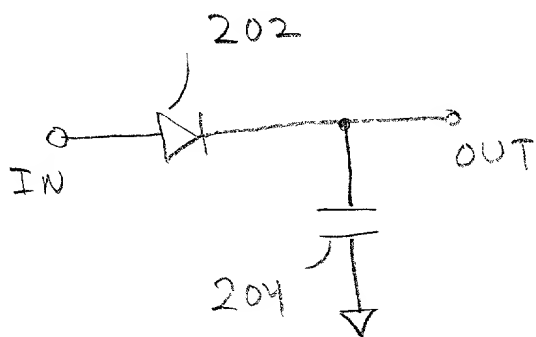


Fig. 2 (PRIOR ART)

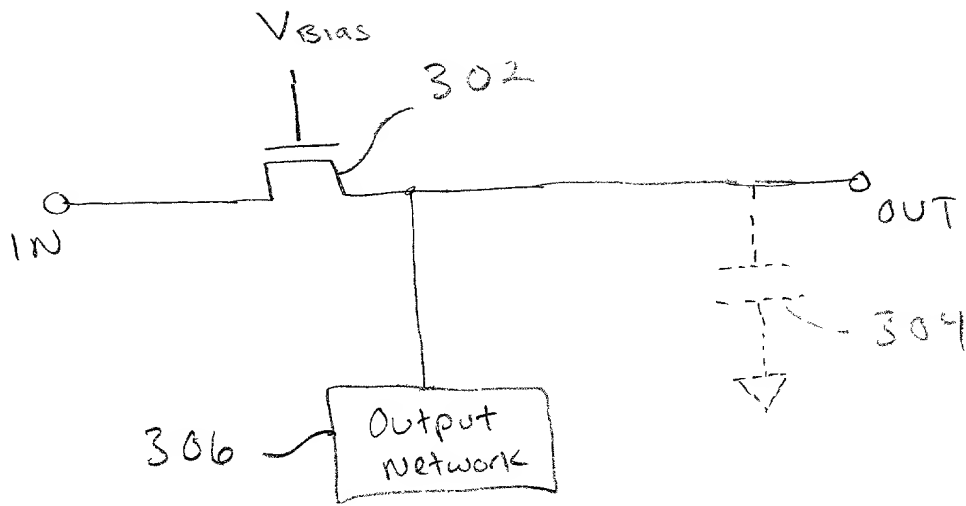


Fig. 3 (PRIOR ART)

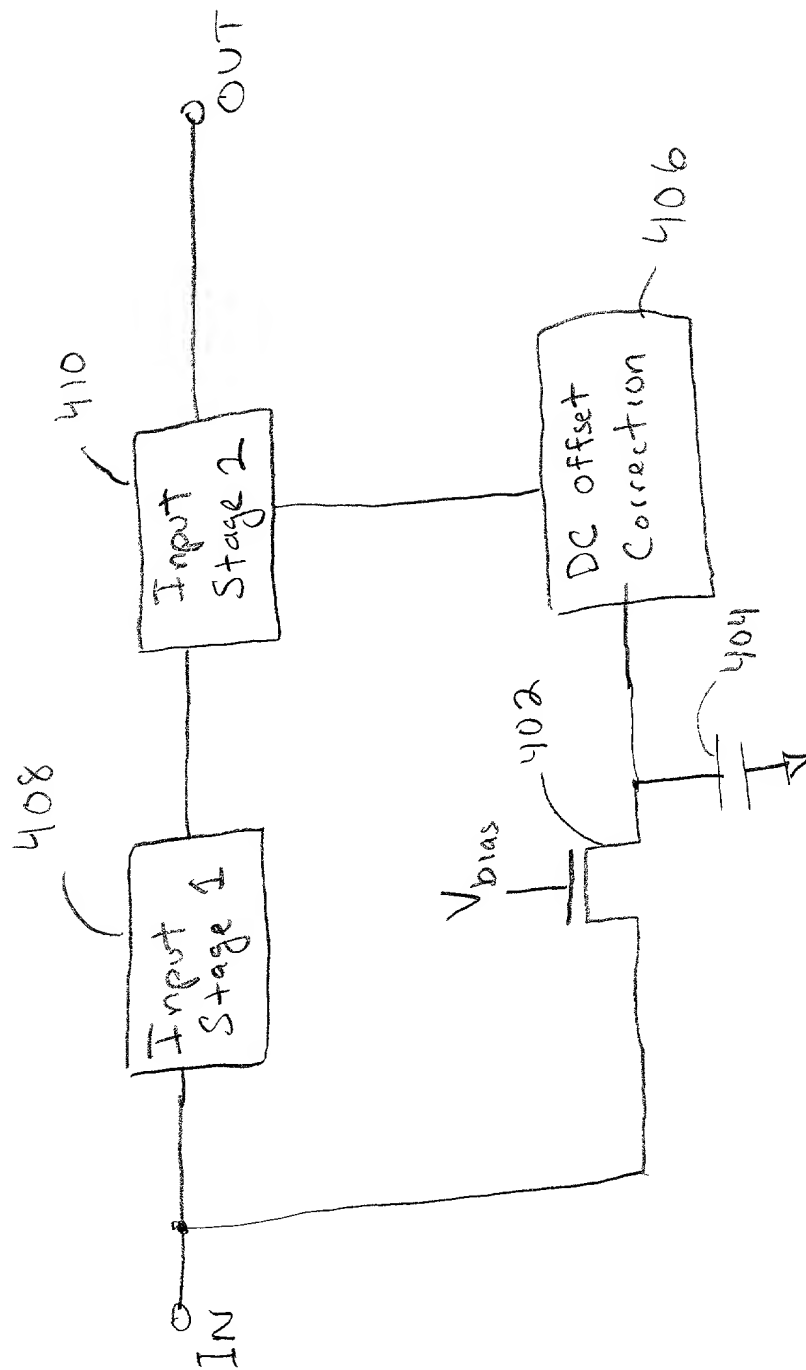


Fig. 4 (Prior Art)

Diagram illustrating a signal processing system 500. The system includes an input signal IN, a block 508, a block 502, a block 510, and an output signal OUT. The input signal IN is processed by block 508, which outputs a stepped waveform. This waveform is then processed by block 502, which outputs a signal that is further processed by block 510 to produce the final output OUT. A dashed line connects block 502 to a block labeled "Output Network" 506. Another dashed line connects block 510 to a dashed line labeled 504.

Fig. 5

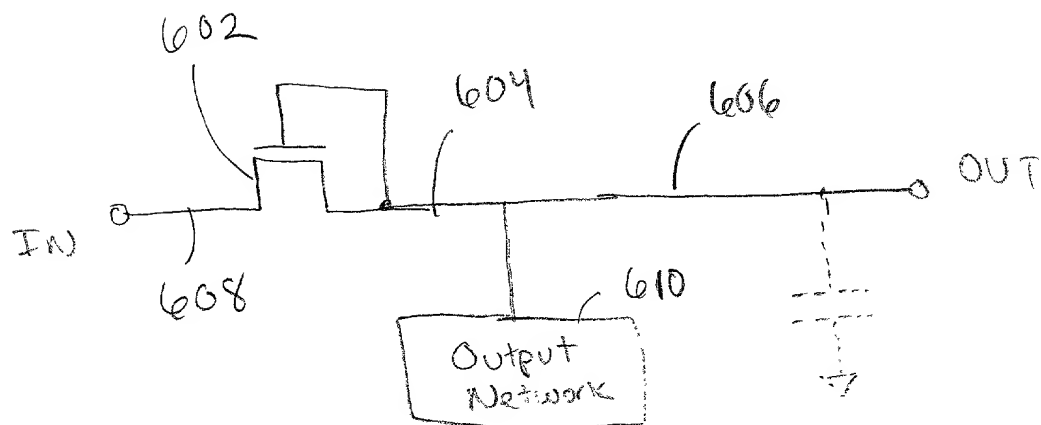


Fig. 6

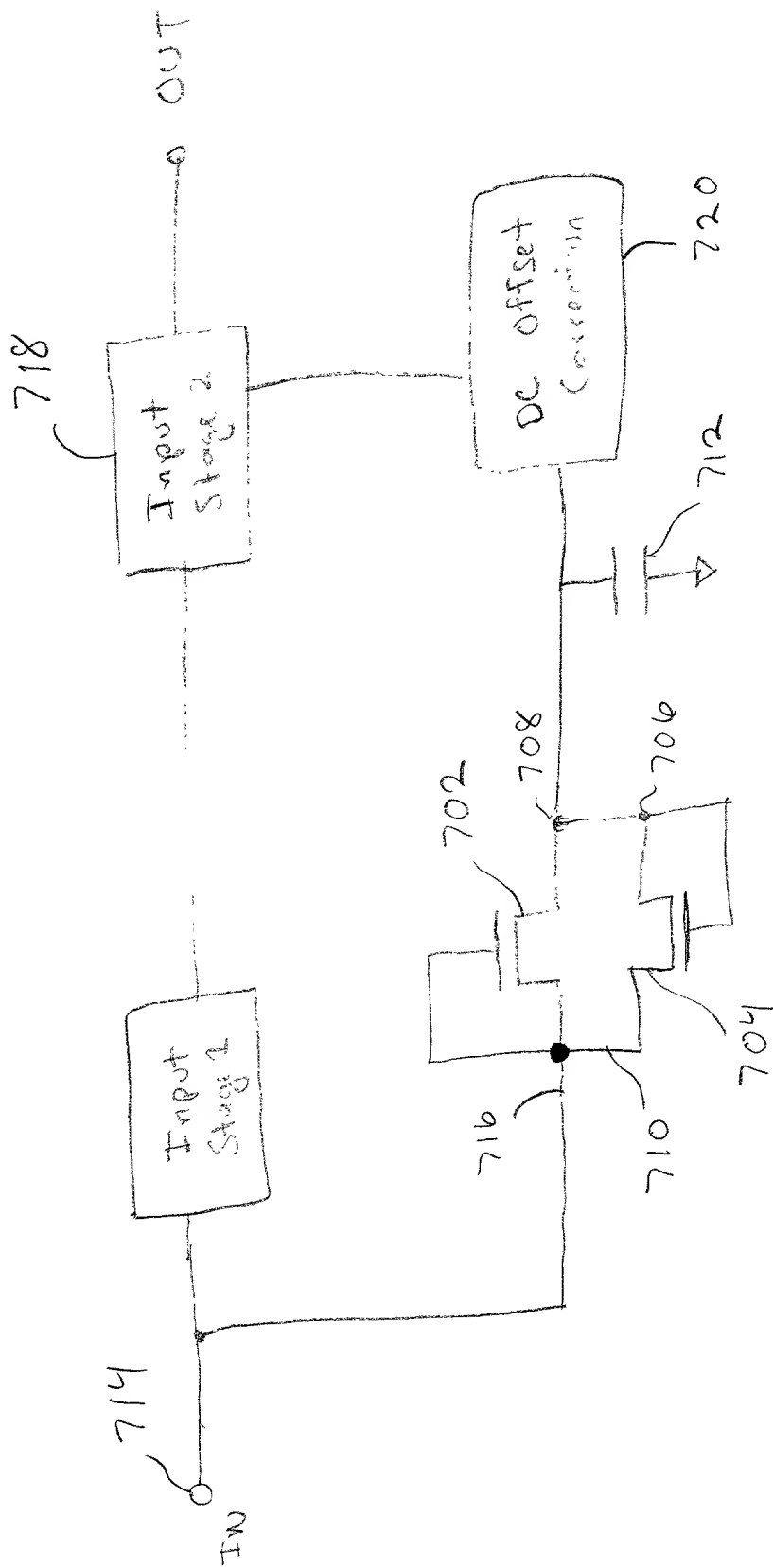


Fig. 7